

Optimizing the Weather Research and Forecasting Model with OpenMP Offload and Codee

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ABSTRACT

Currently, the Weather Research and Forecasting model (WRF) utilizes shared memory (OpenMP) and distributed memory (MPI) parallelisms. To take advantage of GPU resources on Perlmutter, we port parts of the computationally expensive routine Fast Spectral Bin Microphysics (FSBM) to GPUs using OpenMP device offloading. The optimization process is guided by a combination of profilers and a static code inspection tool Codee, resulting in a 1.88x overall speedup for the CONUS-12km winter storm test case.

BACKGROUND

- The Weather Research and Forecasting Model (WRF) is a numerical weather prediction system written in Fortran and is widely used for both research and operational work
- Solves the 3D Euler equations using finite differences, and parallelized through domain decomposition (MPI) and shared work within each domain (OpenMP)
- Computationally expensive routine: Fast Spectral-**Bin Microphysics (FSBM)** scheme computes discrete 33 particle sizes for each grid point (Figure 1)

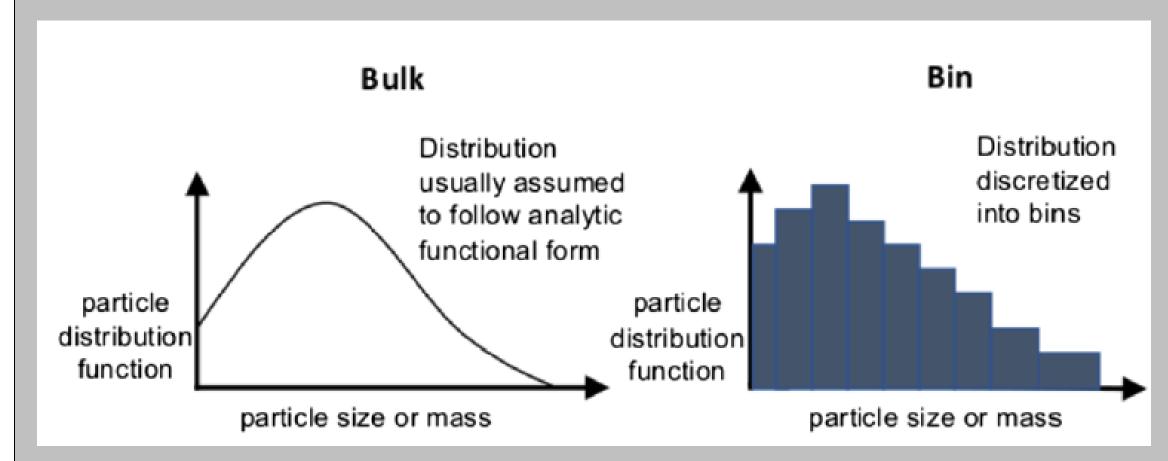


Figure 1: Comparison between bulk microphysics schemes (left) and the more expensive bin schemes (right). Image from Morrison et al, 2020.

• **Codee** is a command-line static code inspection tool for C, C++ and Fortran that analyzes the source code for modernization and performancerelated issues



Explore the use of both profilers and static code analyzer (Codee) to offload expensive routines of WRF to GPUs on Perlmutter through **OpenMP** offload capabilities

METHODOLOGY





2. Get dependency information from Codee and **replace** shared lookup arrays with independent function calls

Codee: Loop modified *!\$omp target teams distribute parallel do &* !\$omp private(n) map(from: cwlg, cwls, ...) ... **do** n = 1.33Codee: Loop modified !\$omp simd Codee detects no loop**do** m = 1.33carried dependencies $ckern_1 = \dots$ $ckern_2 = \dots$ $cwlg(m,n) = (ckern_2 + (ckern1-ckern_2 + ...)) * ...$

3. Offload only outer 2 loops (j,k) due to **large stack usage by** automatic arrays inside the subroutine

4. Replace automatic arrays with slices of global arrays, allowing a **full collapse of the 3 loops**, increasing thread occupancy

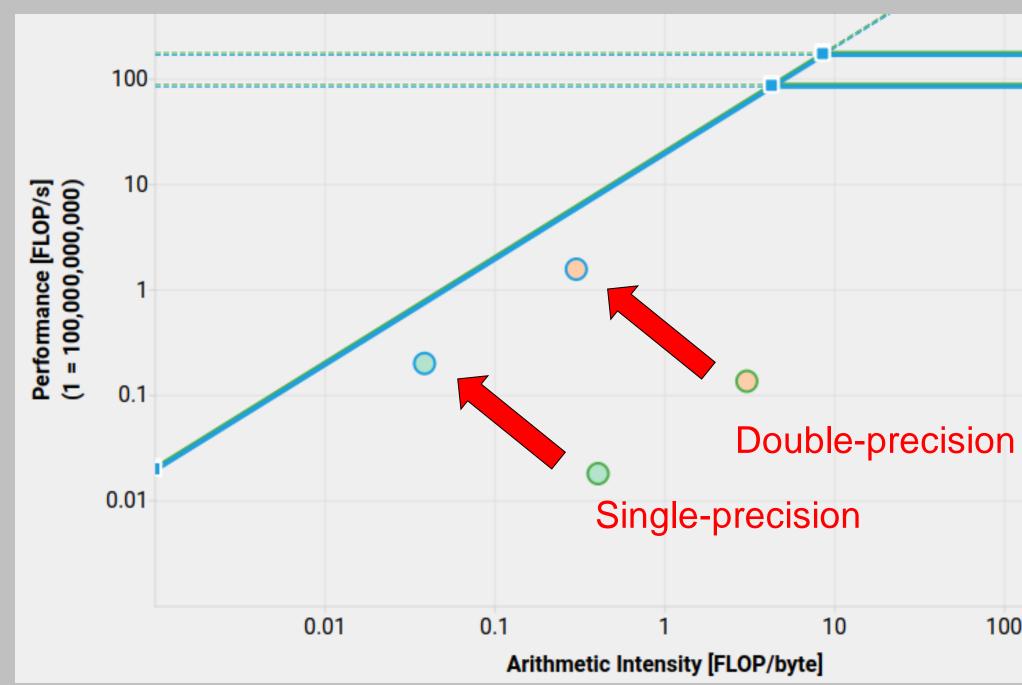


Figure 2: GPU roofline plot comparison of step 3 and 4, showing an increase in FLOP/s from higher occupancy.



RESULTS

- Test case: 10-min simulation of CONUS-12km (2019) Winter Storm) with 325 x 400 x 50 grid points.
- Compiled using PrgEnv-nvidia/8.5.0
- Timings done using 32 MPI ranks and 1 GPU per rank

	Time (s)	Speedup
Baseline	733.7	-
Lookup Optimization	506.2	1.44x
Offloading outer 2 loops	423.3	1.19x
Offloading all 3 loops	388.8	1.08x

Table 1: Wall time and speedup comparison of different steps of optimization

CONCLUSION

- Achieved a total of 1.88x speedup for the CONUS-12km case through porting major FSBM calculations to GPUs with OpenMP
- Explored a workflow for optimization: identifying hotspots with profilers and using Codee to gain more detailed insights of the source code
- Next steps: offload remaining parts of FSBM and reduce host-device data transfers

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